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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/017,666 12/14/2001 PAT 6,686,769

JC

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JC

None

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/06/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>[Signature]</i> Initials <i>JC</i>	STATE OR COUNTRY CA	SHEETS DRAWING 7	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 3
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## TITLE

Programmable I/O element circuit for high speed logic devices

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